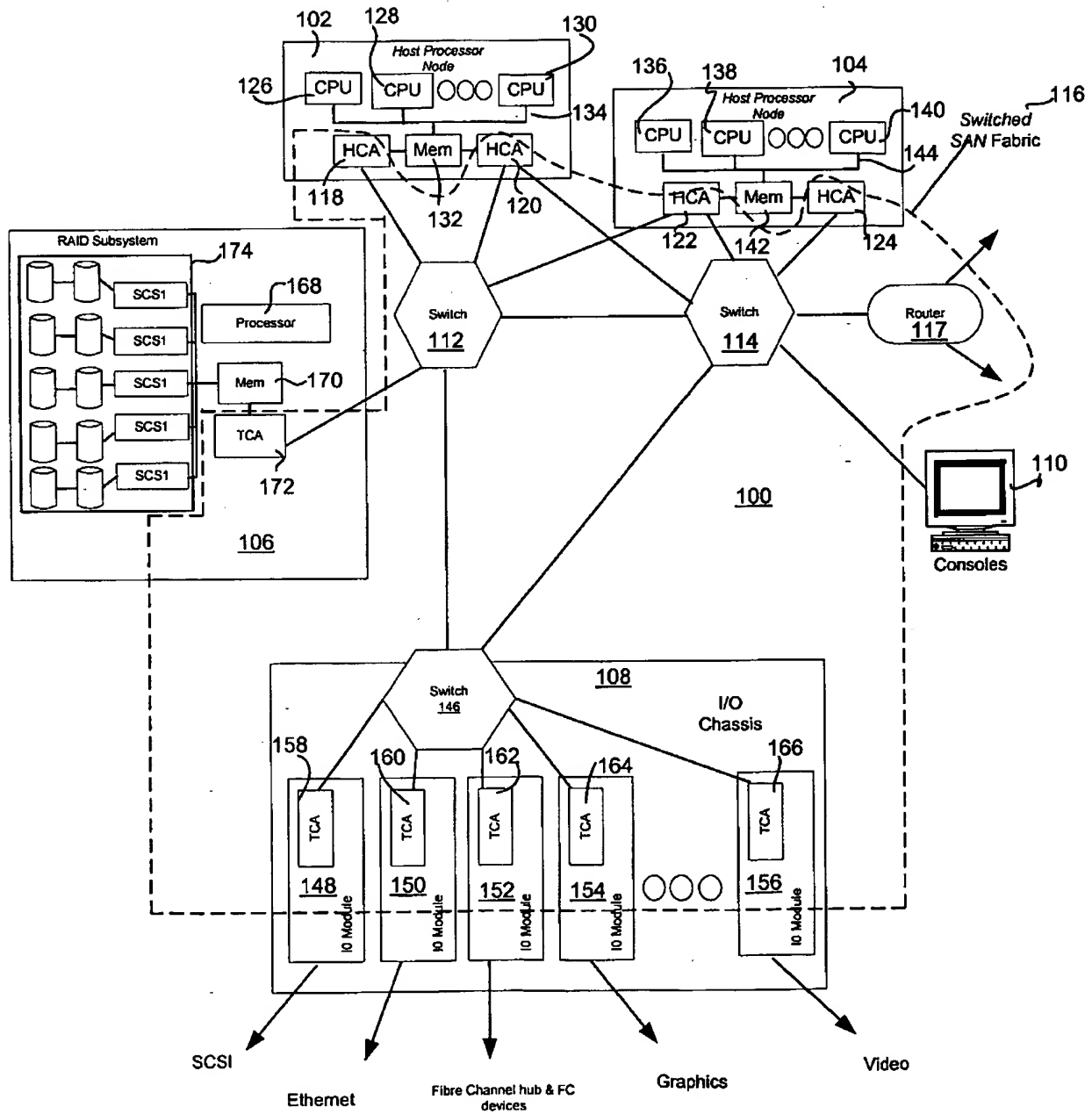


Figure 1

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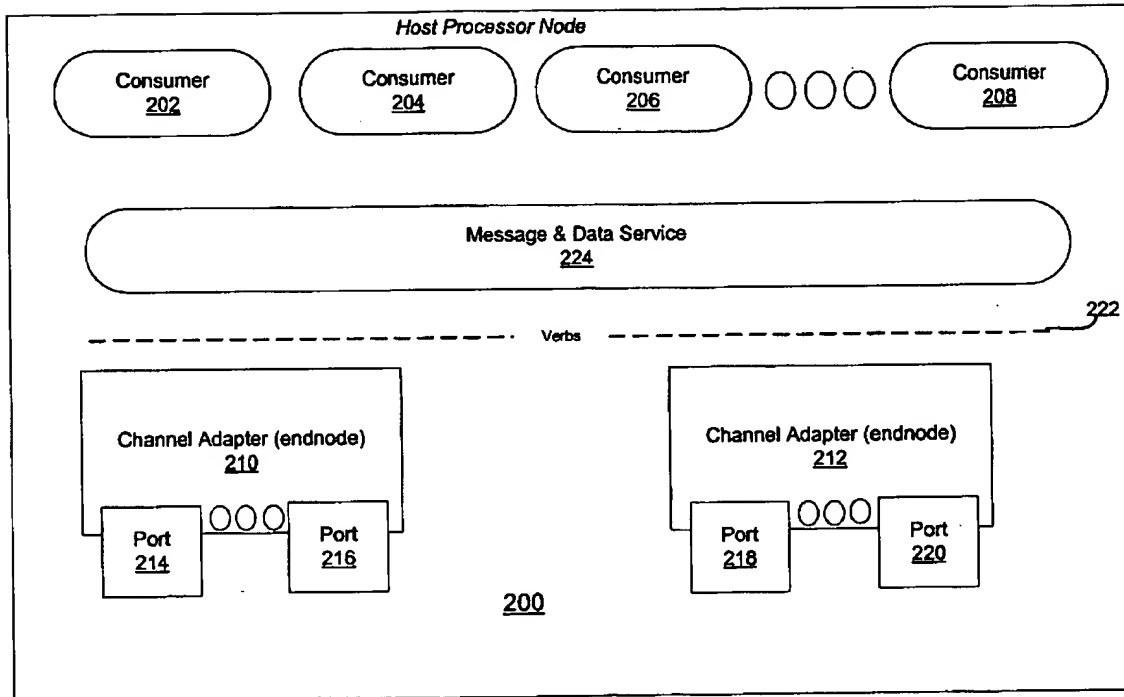


Figure 2

The diagram illustrates a data transfer system. At the top, a large rounded rectangle labeled **Memory 300** contains four vertical blocks labeled **302**, **304**, **306**, and **308**, each with a 'P' and a 'C' symbol. These are connected to a block labeled **310** with a 'P' and a 'C' symbol. Below Memory 300 is a rounded rectangle labeled **DMA 340**. A line labeled **300** connects the DMA 340 to the Memory 300. To the right of DMA 340 is a **Channel Adapter** containing two rounded rectangles labeled **SMA 336** and **MTP 338**. Below the Channel Adapter are three **Port** blocks: **Port 312**, **Port 314**, and **Port 316**. A line labeled **318** connects Port 312 to the Channel Adapter. A line labeled **320** connects Port 312 to Port 314. A line labeled **322** connects Port 314 to Port 316. A line labeled **324** connects Port 314 to the Channel Adapter. A line labeled **326** connects Port 316 to the Channel Adapter. A line labeled **328** connects Port 316 to the Channel Adapter. A line labeled **330** connects the Channel Adapter to Port 316. A line labeled **332** connects the Channel Adapter to Port 316. A line labeled **334** connects the Channel Adapter to Port 316. A line labeled **336** connects the Channel Adapter to Port 316. A line labeled **338** connects the Channel Adapter to Port 316. A line labeled **Transport** connects Port 312 to Port 314.

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Figure 4

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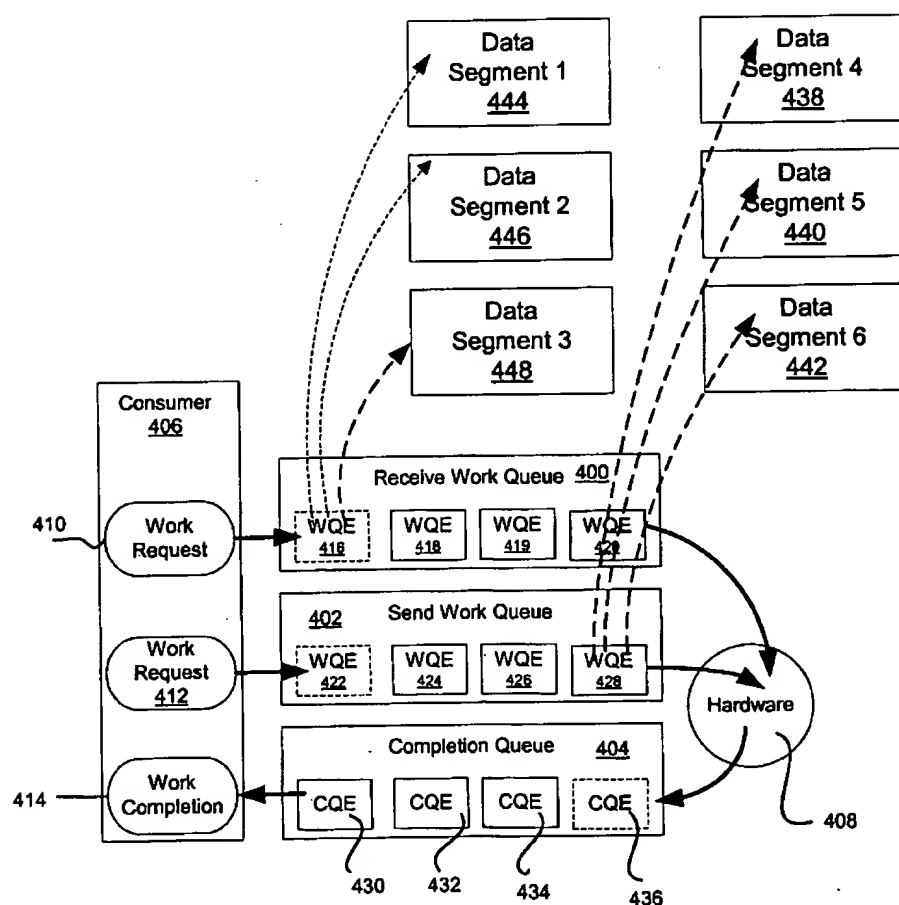
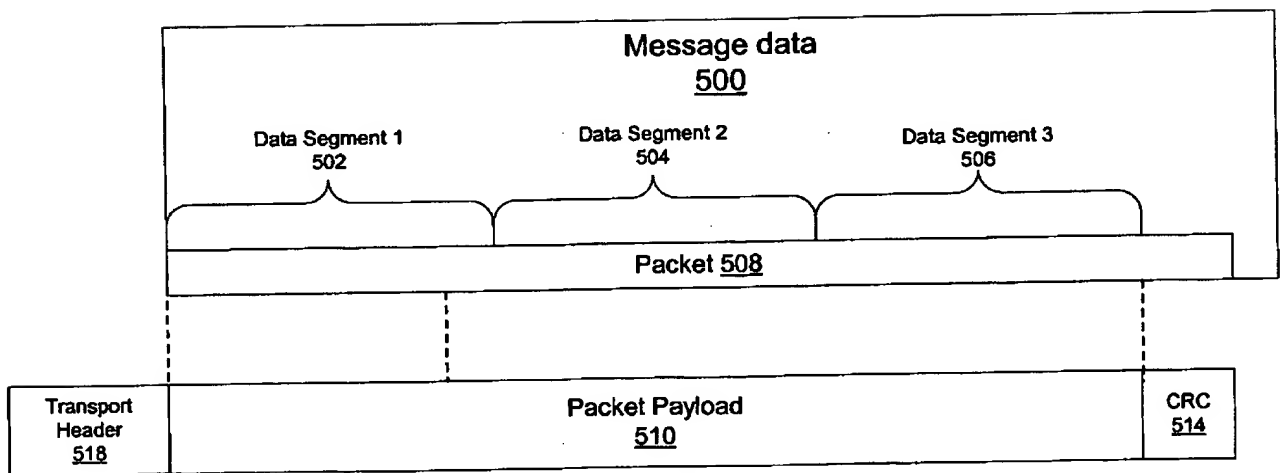


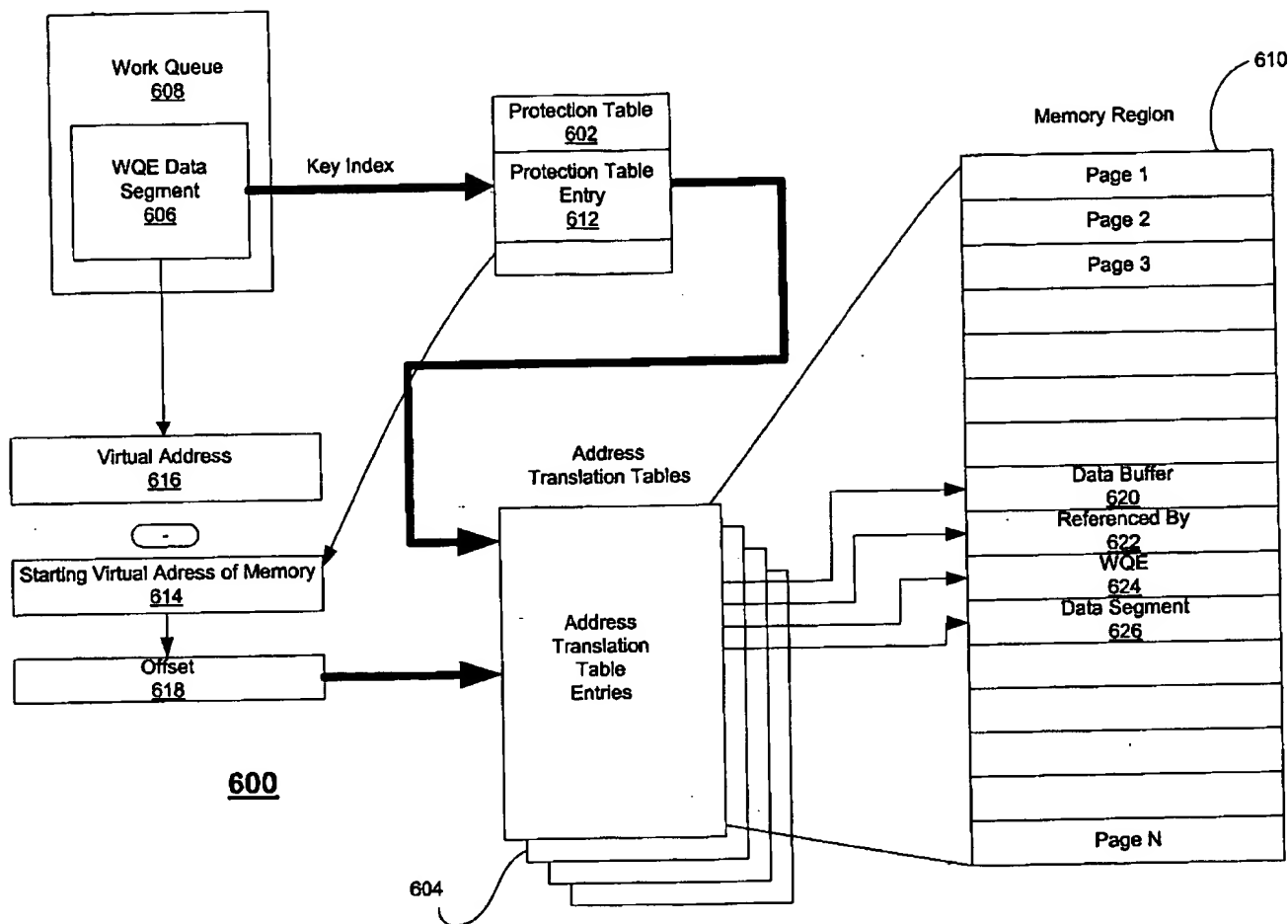
Figure 5

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- 700

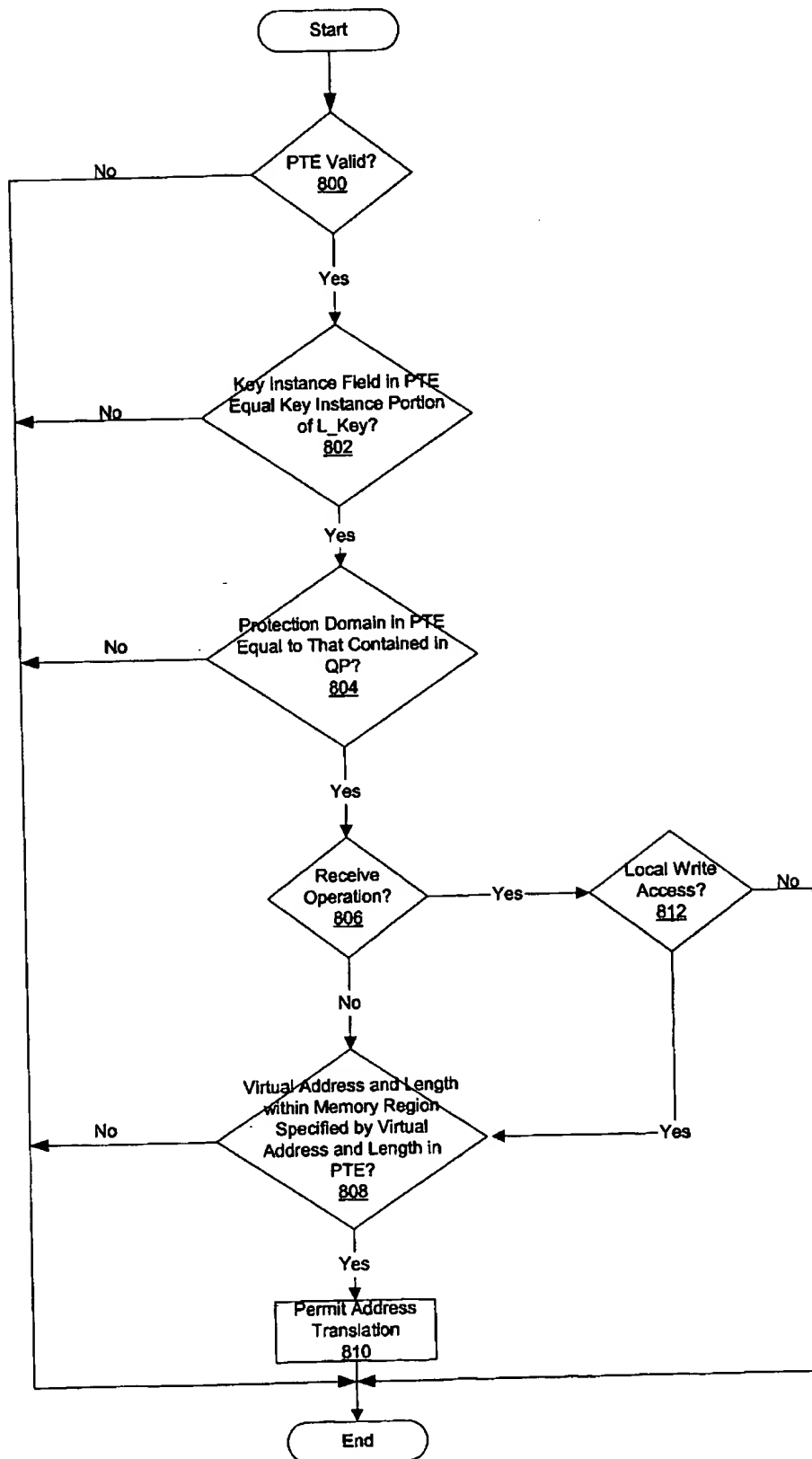
Protection Table Entry

Virtual Address of Start of Memory Region	<u>702</u>
Length of Memory Region	<u>704</u>
Protection Domain	<u>706</u>
Local and Remote Access Control	<u>708</u>
Key_Instance	<u>710</u>
Address Translation Pointer	<u>712</u>

[illegible]

Figure 8

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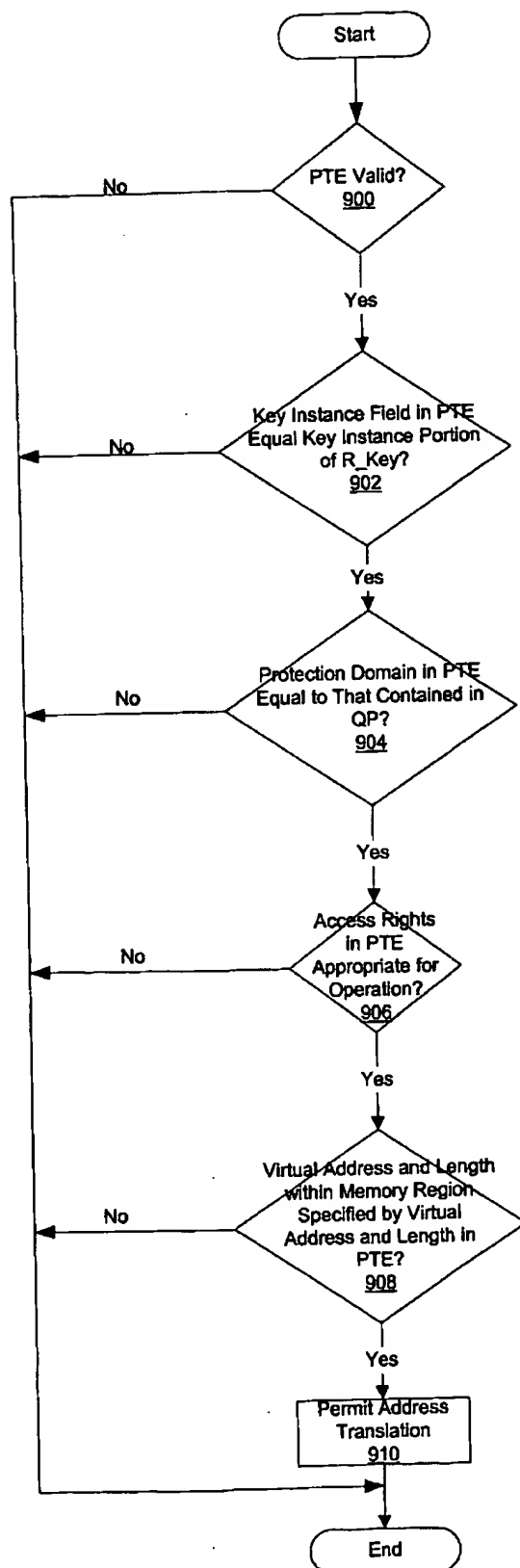


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[illegible]